

# IRF7853PbF

HEXFET® Power MOSFET

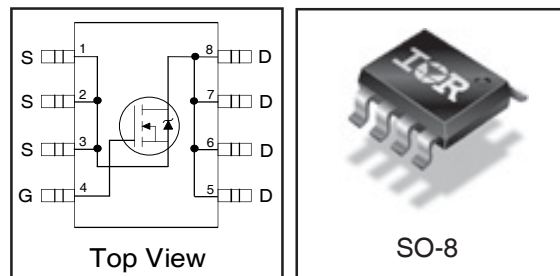
## Applications

- Primary Side Switch in Bridge Topology in Universal Input (36-75Vin) Isolated DC-DC Converters
- Primary Side Switch in Push-Pull Topology for 18-36Vin Isolated DC-DC Converters
- Secondary Side Synchronous Rectification Switch for 15Vout
- Suitable for 48V Non-Isolated Synchronous Buck DC-DC Applications

## Benefits

- Low Gate to Drain Charge to Reduce Switching Losses
- Fully Characterized Capacitance Including Effective  $C_{OSS}$  to Simplify Design, (See App. Note AN1001)
- Fully Characterized Avalanche Voltage and Current

$V_{DSS}$	$R_{DS(on) \max}$	$I_D$
100V	18m $\Omega$ @ $V_{GS} = 10V$	8.3A



## Absolute Maximum Ratings

	Parameter	Max.	Units
$V_{DS}$	Drain-to-Source Voltage	100	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	8.3	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	6.6	
$I_{DM}$	Pulsed Drain Current ①	66	
$P_D @ T_A = 25^\circ C$	Maximum Power Dissipation	2.5	W
	Linear Derating Factor	0.02	W/ $^\circ C$
dv/dt	Peak Diode Recovery dv/dt ②	5.1	V/ns
$T_J$	Operating Junction and	-55 to + 150	$^\circ C$
$T_{STG}$	Storage Temperature Range		

## Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JL}$	Junction-to-Drain Lead	—	20	$^\circ C/W$
$R_{\theta JA}$	Junction-to-Ambient (PCB Mount) ③ ④	—	50	

Notes ① through ④ are on page 8

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## Static @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	100	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.11	—	V/°C	Reference to $25^\circ\text{C}, I_D = 1\text{mA}$
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	14.4	18	m $\Omega$	$V_{GS} = 10V, I_D = 8.3A$ ④
$V_{GS(th)}$	Gate Threshold Voltage	3.0	—	4.9	V	$V_{DS} = V_{GS}, I_D = 100\mu A$
$I_{DSS}$	Drain-to-Source Leakage Current	—	—	20	$\mu A$	$V_{DS} = 100V, V_{GS} = 0V$
		—	—	250		$V_{DS} = 100V, V_{GS} = 0V, T_J = 125^\circ\text{C}$
$I_{GSS}$	Gate-to-Source Forward Leakage	—	—	100	nA	$V_{GS} = 20V$
	Gate-to-Source Reverse Leakage	—	—	-100		$V_{GS} = -20V$

## Dynamic @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

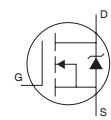
	Parameter	Min.	Typ.	Max.	Units	Conditions
gfs	Forward Transconductance	11	—	—	S	$V_{DS} = 25V, I_D = 5.0A$
$Q_g$	Total Gate Charge	—	28	39	nC	$I_D = 5.0A$ $V_{DS} = 50V$ $V_{GS} = 10V$ ④
$Q_{gs}$	Gate-to-Source Charge	—	7.8	—		
$Q_{gd}$	Gate-to-Drain ("Miller") Charge	—	10	—		
$R_G$	Gate Resistance	—	1.4	—		
$t_{d(on)}$	Turn-On Delay Time	—	13	—	ns	$V_{DD} = 50V$ $I_D = 5.0A$ $R_G = 6.2\Omega$ $V_{GS} = 10V$ ④
$t_r$	Rise Time	—	6.6	—		
$t_{d(off)}$	Turn-Off Delay Time	—	26	—		
$t_f$	Fall Time	—	6.0	—		
$C_{iss}$	Input Capacitance	—	1640	—	pF	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1.0\text{MHz}$ $V_{GS} = 0V, V_{DS} = 1.0V, f = 1.0\text{MHz}$ $V_{GS} = 0V, V_{DS} = 80V, f = 1.0\text{MHz}$ $V_{GS} = 0V, V_{DS} = 0V \text{ to } 80V$ ⑤
$C_{oss}$	Output Capacitance	—	310	—		
$C_{rss}$	Reverse Transfer Capacitance	—	71	—		
$C_{oss}$	Output Capacitance	—	1600	—		
$C_{oss}$	Output Capacitance	—	180	—		
$C_{oss \text{ eff.}}$	Effective Output Capacitance	—	320	—		

## Avalanche Characteristics

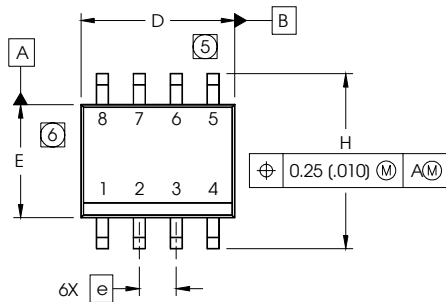
	Parameter	Typ.	Max.	Units
$E_{AS}$	Single Pulse Avalanche Energy ②	—	610	mJ
$I_{AR}$	Avalanche Current ①	—	5.0	A

## Diode Characteristics

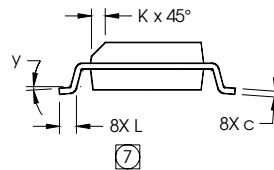
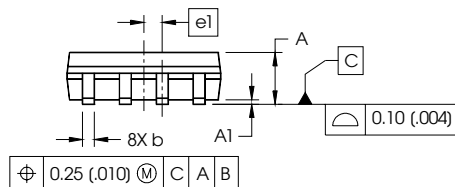
	Parameter	Min.	Typ.	Max.	Units	Conditions
$I_S$	Continuous Source Current (Body Diode)	—	—	2.3	A	MOSFET symbol showing the integral reverse p-n junction diode.
$I_{SM}$	Pulsed Source Current (Body Diode) ①	—	—	66		
$V_{SD}$	Diode Forward Voltage	—	—	1.3	V	$T_J = 25^\circ\text{C}, I_S = 5.0A, V_{GS} = 0V$ ④
$t_{rr}$	Reverse Recovery Time	—	45	68	ns	$T_J = 25^\circ\text{C}, I_F = 5.0A, V_{DD} = 25V$
$Q_{rr}$	Reverse Recovery Charge	—	84	130	nC	$di/dt = 100A/\mu s$ ④
$t_{on}$	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				



## SO-8 Package Details



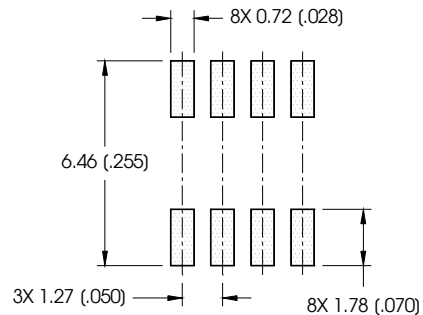
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050 BASIC		1.27 BASIC	
e1	.025 BASIC		0.635 BASIC	
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.050	0.40	1.27
y	0°	8°	0°	8°



**NOTES:**

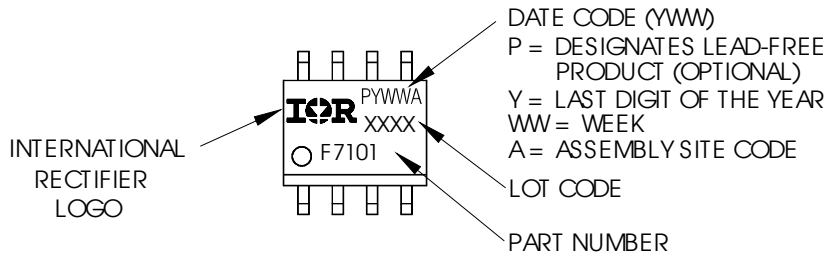
1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: MILLIMETER
3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- ⑤ DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 (.006).
- ⑥ DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.010).
- ⑦ DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.

**FOOTPRINT**



## SO-8 Part Marking

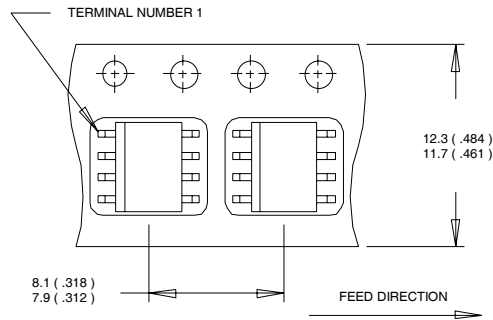
EXAMPLE: THIS IS AN IRF7101 (MOSFET)



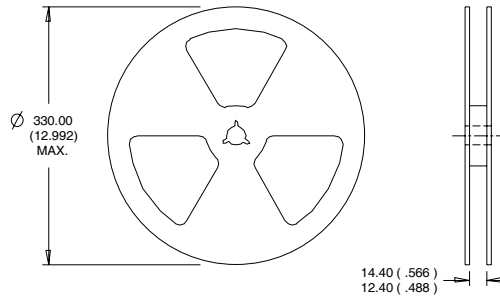
# IRF7853PbF

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## SO-8 Tape and Reel



- NOTES:
1. CONTROLLING DIMENSION : MILLIMETER.
  2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
  3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



- NOTES :
1. CONTROLLING DIMENSION : MILLIMETER.
  2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

### Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting  $T_J = 25^\circ\text{C}$ ,  $L = 49\text{mH}$ ,  $R_G = 25\Omega$ ,  $I_{AS} = 5.0\text{A}$ .
- ③ When mounted on 1 inch square copper board,  $t \leq 10$  sec.
- ④ Pulse width  $\leq 400\mu\text{s}$ ; duty cycle  $\leq 2\%$ .
- ⑤  $C_{oss}$  eff. is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .
- ⑥  $I_{SD} \leq 5.0\text{A}$ ,  $di/dt \leq 320\text{A}/\mu\text{s}$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J \leq 150^\circ\text{C}$ .
- ⑦  $R_{\theta}$  is measured at  $T_J$  of approximately  $90^\circ\text{C}$ .

Data and specifications subject to change without notice.  
This product has been designed and qualified for the Industrial market.

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